

## (12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date  
15 July 2004 (15.07.2004)

PCT

(10) International Publication Number  
WO 2004/059730 A1

(51) International Patent Classification<sup>7</sup>: H01L 23/12, 23/48

(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.

(21) International Application Number: PCT/US2002/040307

(22) International Filing Date: 18 December 2002 (18.12.2002)

(25) Filing Language: English

(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

(26) Publication Language: English

## Declaration under Rule 4.17:

— of inventorship (Rule 4.17(iv)) for US only

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## Published:

— with international search report

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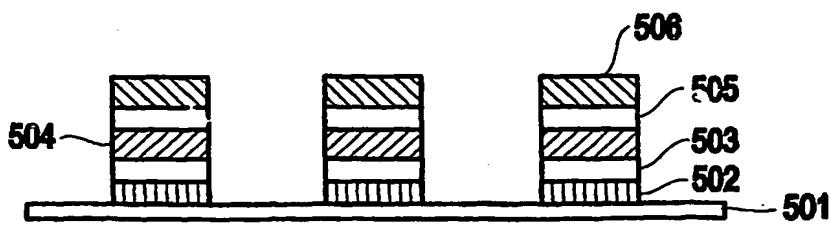
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WO 2004/059730 A1

(54) Title: METHOD OF SELF-ASSEMBLING ELECTRONIC CIRCUITRY AND CIRCUITS FORMED THEREBY



(57) Abstract: A method of assembling a circuit includes providing a template, enabling a semiconductor material to self assemble on the template, and enabling self-assembly of a connection between the semiconductor material and the template to form the circuit and a circuit created by self-assembly.

# INTERNATIONAL SEARCH REPORT

International application No.

PCT/US02/40307

## A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : H01L 23/12, 23/48  
US CL : 257/700, 703, 741, 758-760

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 257/700, 703, 741, 758-760

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

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## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6,407,443 B2 (Chen et al.) 18 June 2002 (18.06.2002), col. 3 lines 42-59	1, 7, 13
A	US 6,340,822 B1 (Brown et al.) 22 January 2002 (22.01.2002), col. 3 lines 3 lines 48-67 and col. 4 lines 25-65.	1-18
A	US 2001/0023986 A1 (Mancevski) 27 September 2001 (27.09.01), paragraph 40-42.	1-18

Further documents are listed in the continuation of Box C.  See patent family annex.

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Date of the actual completion of the international search

24 FEBRUARY 2003

Date of mailing of the international search report

14 MAY 2003

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Commissioner of Patents and Trademarks  
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